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Attorney Docket no: SEL 239

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Konuma et al.)
)
Serial No.: 09/782,239)
)
Filed: February 13, 2001)
)
For: Self-Light-Emitting Device And Method)
Of Manufacturing The Same)
)
Examiner: D. Wille)
)
Art Unit: 2814)
)
Confirm no.: 9357)

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Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on 5/26/04

Shannon Wallace

Date: May 26, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST TO CONSIDER PREVIOUSLY SUBMITTED IDS
IN FURTHERANCE OF NOTICE OF ALLOWANCE**

Sir:

On April 8, 2004, the undersigned filed an IDS and a 1449 form (see attached copy with references), with a certificate of mailing. As the attached post card shows, this IDS was marked received by the PTO on April 12, 2004.

On April 28, 2004, the Examiner issued a Notice of Allowance for the above-identified application which Applicants appreciate. However, upon review of the Notice of Allowance, Applicants note that the Examiner has not considered the IDS filed on April 8, 2004.

As the date of filing of this IDS is prior to the date of the Notice of Allowance, Applicants request that the Examiner consider this IDS and issue a supplemental Notice of Allowance.

Applicants do not believe a petition or fee is due for this request. However, if a petition is due, please consider this such a petition and please charge our deposit account 50/1039 for any fee due for this request or petition.

Favorable consideration is earnestly solicited.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Mark J. Murphy', with a stylized, flowing script.

Mark J. Murphy
Registration No. 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, Ltd.
200 West Adams Street, Suite 2850
Chicago, Illinois 60606
(312) 236-8500



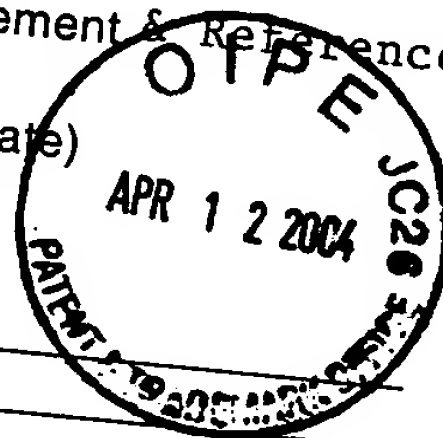
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HERETO, ACKNOWLEDGES RECEIPT OF:

ENCLOSURES:

- ☐ Application _____
- ☐ Specification _____ pages
- ☐ Claims _____ pages
- ☐ Abstract _____ page(s)
- ☐ Drawing Sheets no. _____ (F) _____ (INF) _____
- ☐ Transmittal Letter
- ☐ Check \$ _____ No: _____
- ☐ Declaration/Oath
- ☐ Assignment and Cover Sheet
- ☒ Information Disclosure Statement & References
- ☐ Response/Amendment
- ☐ Extension of Time (In Duplicate)
- ☐ Small Entity Status
- ☐ Copy of Priority Document
- ☐ _____
- ☐ _____



RE: APPLICATION

ATTY/SEC: MJM/cn

File no: 0553-0239

Applicant: Tomshimitsu KONUMA et al

S.N.: 09/782,239 Filing Date: February 13, 2001

Title: Self-Light-Emitting Device and
Method of Manufacturing the Same

Due date: _____ Date Sent: 4/8/2004

SEL

CASE	0553-0239
ATTY	EDM/MJM
DATE	
FILED	
RECEIVED	
OK TO FILE	

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Tomshimitsu KONUMA et al

Serial No.: 09/782,239

Filed: February 13, 2001

Art Unit: 2814

Examiner: Douglas A. Wille

For: SELF-LIGHT-EMITTING DEVICE AND
METHOD OF MANUFACTURING THE SAME

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is being deposited with the United States
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2004

Cristine m. noll

Date: April 8, 2004

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. Copies of the references are enclosed herewith for the Examiner's consideration.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

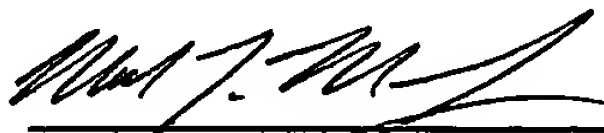
The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not

possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

As a RCE was filed no fee is believed necessary. If a fee is required, please charge Deposit Account No. 50/1039.

Respectfully submitted,



Mark J. Murphy
Registration No: 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500

LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Atty Docket No.</u> SEL 239	<u>Serial No.</u> 09/782,239
	<u>Applicant</u> Toshimitsu KONUMA et al	
	<u>Filing Date</u> February 13, 2001	<u>Group</u> 2814

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,895,228	04/20/99	Biebuyck et al	438	99	03/20/97

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE
	JP 07-258410	10/09/95	UBE Ind. Ltd.	X		03/18/94
	EP 0 768 352	04/16/97	Hitachi Chem Co.			06/30/95
	EP 0 881 668 A2	12/02/98	Dow Corning Toray Silicone Co. Ltd.			05/27/98
	EP 0 893 485	01/27/99	Sumitomo Chem Co.			07/22/98
	EP 0 899 987	03/03/99	TDK Corp.			08/28/98

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

	1) European Search Report re application no. EP 03 02 0964, mailed December 3, 2003. 2) FRITSCH, U. et al, "A Submicron CMOS Two Level Metal Process with Planarization Techniques," V-MIC Conf., June 13-14, 1988 IEEE, pp. 69-75 (1988). 3) IBARAKI, N. et al, "A New a-Si TFT with SiO ₂ /SiN _x Gate Insulator for 10.4 inch LCDs," Proceedings of the International Display Research Conference, IEEE, pp. 97-100, (1991). 4) PRAMANIK, D. et al, "A High Reliability Triple Metal Process for High Performance Application Specific Circuits," VLSI Multilevel Interconnection Conference, June 11-12, 1991, pp. 27-33, (1991).
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EXAMINER:	DATE CONSIDERED:
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.

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